



# CHRIST THE KING ENGINEERING COLLEGE

Karamadai, Coimbatore - 641104

(Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai)  
Ph: +91 91500 76729 Email: principal@ckec.ac.in Web: www.ckec.ac.in

## PERMISSION LETTER

Date: 12-07-2018

### From

Mr.D. Citharthan,  
Assistant Professor / Course Coordinator,  
Department of Electrical and Electronics Engineering,  
Christ the King Engineering College,  
Coimbatore.

### To

The Principal,  
Christ the King Engineering College,  
Coimbatore.

Through : Head of The Department / EEE

### Respected Sir,

Sub: Requesting permission to organize a Value Added Course on "Simulation of Basic Electronic Circuits and PCB Design using Easy EDA tool" - Reg.

Department of Electrical and Electronics Engineering is planning to organize a Value Added Course on "Simulation of Basic Electronic Circuits and PCB Design using Easy EDA tool" from 18-07-2018 to 03-08-2018 for II, III & IV EEE students. Mr.G. Gokul kumar, Assistant Professor, Department of Electrical and Electronics Engineering as a Resource Person. In this regard, we seek your permission to conduct the same in the power system simulation lab from 18-07-2018 to 03-08-2018 from 4 pm to 7 pm. Kindly consider our request and grant the same.

Thanking You,



*M. Jeyakumar*  
**Dr.M.JEYAKUMAR, M.E..Ph.D.**  
PRINCIPAL  
CHRIST THE KING ENGINEERING COLLEGE,  
Chikkarampalayam Village,  
Karamadai, Mertupalayam Taluk,  
Coimbatore - 641 104.

Yours truly,

*M. Jeyakumar*

Approved

*M. Jeyakumar*



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Ref.No: CKEC/EEE/VAC/18-19/001

DATE: 16-07-2018

### CIRCULAR

This is to inform that the department of Electrical and Electronics engineering is planning to conduct a value-added course on "SIMULATION OF BASIC ELECTRONIC CIRCUITS AND PCB DESIGN USING EASY EDA TOOL" for all II, III & IV EEE students on 18-07-2018 to 03-08-2018 students are asked to register you and utilize this opportunity to improve their skills.

S. No	Course Code	Name of the Course	Department	No of Hours	Course Coordinator
1	VAEE1801	SIMULATION OF BASIC ELECTRONIC CIRCUITS AND PCB DESIGN USING EASY EDA TOOL	EEE	35	Mr.D. Citharthan, AP/EEE

Note: The circular Display in the department Notice board for student's attention.

Copy to:  
Principal's Desk  
Notice board

*m.j.u*  
HoD / EEE



*[Signature]*  
**Dr.M.JEYAKUMAR, M.E.,P.D.**  
PRINCIPAL  
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Chikkarapalayam Village,  
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## CHRIST THE KING ENGINEERING COLLEGE



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### DEPARTMENT OF EEE

Organizes

**SIMULATION OF BASIC ELECTRONIC CIRCUITS  
AND PCB DESIGN USING EASY EDA TOOL**

**18-7-2018 TO 3-8-2018 (4 TO 7 PM)**

#### Resource Person

**Mr .G.GOKUL KUMAR  
ASSISTANT PROFESSOR  
DEPARTMENT OF EEE**



Chief Patron  
Dr. M. Jeyakumar  
Principal

Patron  
Mr. M. Arumuga Babu  
HOD / EEE

Coordinator  
Mr. D. Citharthan  
AP / EEE

Venue:  
PSS Lab

*M. Jeyakumar*  
**Dr.M.JEYAKUMAR, M.E.,Ph.D**  
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Course Code	VAEE1801
Name of the Course	SIMULATION OF BASIC ELECTRONIC CIRCUITS AND PCB DESIGN USING EASY EDA TOOL
Duration of the Course	35 Hours
Academic Year	2018-2019
Period of the Course	15 Days

### COURSE OBJECTIVES:

- To enable the students to understand the basic electronic circuits.
- To understand the single sided PCB and double sided PCB.
- To explore Easy EDA - Online PCB design & circuit simulator
- To create DIY PCB

### SYLLABUS

#### MODULE 1 - BASIC ELECTRONIC DEVICES & CIRCUITS (7)

Diodes – Applications – Rectifier – Half wave rectifier – Full wave rectifier – Full wave bridge rectifier Transistors – BJT and MOSFET – Biasing circuit - Applications – Through hole components and Surface mounted components.

#### MODULE 2 - INTRODUCTION TO PCB (7)

Printed circuit board – Single Sided PCB and Double Sided PCB – Substrate – Copper foil layer – Solder mask – Silk Screen – Hole – Pad - Via – Track – Jumper – Ground plane / Copper Pour / Solid Region – Footprint / Package – CAD tools for designing PCB.

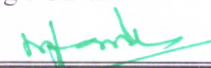
#### MODULE 3 - EXPLORING EASY EDA (8)

Advantages of Easy EDA – Simulation mode – Simulation of BJT as a switch – Standard mode – wiring tools – Drawing tools – Schematic of basic electronic circuits using through hole components (single sided PCB) – Schematic of basic electronic circuits using through hole and surface mounted components (double sided PCB) – Creating custom part in Easy EDA.

#### MODULE 4 - PCB LAYOUT AND ARTWORK USING EASY EDA (7)

Layout of single sided and double sided PCB – Artwork of single sided and double sided PCB – Grounding plane and its uses - Generating Gerber file – Parameters to check for ordering PCB in Easy EDA and other platforms.



  
Dr. M. JEYAKUMAR, M.E., Ph.D.  
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## MODULE 5 – DIY PCB

(6)

Artwork Printing – Etching Process – Drilling – Soldering – Testing.

### COURSE OUTCOMES:

At the end of the course, the students will be able

- To learn the fundamentals explore Easy EDA - Online PCB design & circuit simulator
- To understand the single sided PCB and double sided PCB and create DIY PCB.

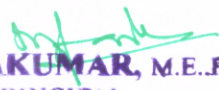
### TEXTBOOKS:

1. "Electronic Principles" by Albert Malvino and David Bates.
2. "Circuit Analysis": Theory and Practice" by Allan H. Robbins and Wilhelm C. Miller.
3. "EasyEDA for Electronic Circuit Design and Simulation" by Fei Ling.

### REFERENCES:

1. "EasyEDA Simulation Cookbook" by James M. Fiore.
2. "PCB Currents: How They Flow, How They React" by Douglas Brooks.



  
**Dr. M. JEYAKUMAR, M.E..Ph.D.**  
**PRINCIPAL**  
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING VAEE1801 - Simulation of Basic Element Circuits and PCB Design using EDA Tool

### SCHEDULE

S. No	DAY	TIME/SESSION	CONTENT	RESOURCE PERSON
1	18-07-2018	4:00-7:00 pm	BASIC ELECTRONIC DEVICES & CIRCUITS	<b>Mr.G. Gokul kumar</b> Assistant Professor, Department of Electrical and Electronics Engineering, Christ the King Engineering College
	19-07-2018	4:00-8:00 pm		
2	20-07-2018	4:00-6:00 pm	INTRODUCTION TO PCB	
	21-07-2018	4:00-6:00 pm		
	23-07-2018	4:00-7:00 pm		
3	24-07-2018	4:00-6:00 pm	EXPLORING EASY EDA	
	25-07-2018	4:00-7:00 pm		
	26-07-2018	4:00-7:00 pm		
4	27-07-2018	4:00-6:00 pm	PCB LAYOUT AND ARTWORK USING EASY EDA	
	28-07-2018	4:00-6:00 pm		
	30-07-2018	4:00-7:00 pm		
5	31-07-2018	4:00-6:00 pm	DIY PCB	
	01-08-2018	4:00-6:00 pm		
	02-08-2018	4:00-6:00 pm	ASSESSMENT	
	03-08-2018	4:00-5:00 pm		

*m.j.w*  
HoD / EEE



*m.j.w*  
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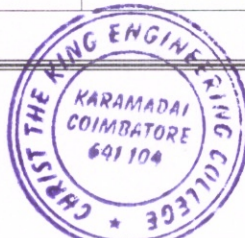
Email: principal@ckec.ac.in

Web: www.ckec.ac.in

## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING VAEE1801 - Simulation of Basic Element Circuits and PCB Design using EDA Tool STUDENT REGISTRATION

S.NO.	REGISTER NUMBER	NAME OF THE STUDENT	SIGNATURE
1	710417105001	ABINAYA M	Abinaya
2	710417105002	ASWINI R	Aswini R
3	710417105004	DIVYA R	Divya R
4	710417105005	GEETHA V	Geetha V
5	710417105006	GOMATHI P	Gomathi P
6	710417105009	NIVETHA S	Nivetha S
7	710417105010	SOWMIYA S	Sowmiya S
8	710417105011	SUBHASH B	Subhash B
9	710416105001	ABUTHAHEER.M	Abuthaheer.M
10	710416105002	AKSHAY BENNY.K	Akshay Benny.K
11	710416105003	ANJITHA.K.J	Anjitha.K.J
12	710416105004	ASWATHI.P.D	Aswathi.P.D
13	710416105005	BAISE.V.B	Baise.V.B
14	710416105006	DENNIS ALDIRAN .A	Dennis Aldiran .A
15	710416105007	GRACE.K	Grace.K
16	710416105008	KEVIN LOUIS.G	Kevin Louis.G
17	710416105009	RENU.B	Renu.B
18	710416105011	VIJAY KUMAR.S	Vijay Kumar.S
19	710416105301	AJITH.S.R	Ajith.S.R
20	710415105001	AJANTHA N	Ajantha N
21	710415105002	AKHIL MATHEW	Akhil Mathew
22	710415105003	ALAN J WALTER	ALAN J
23	710415105004	ALDRIN THOMAS	ALDRIN THOMAS
24	710415105005	BURUSLEE A	Buruslee A
25	710415105006	JAYASHREE P	Jayashree P
26	710415105007	JITHIN RAJ	Jithin Raj
27	710415105008	JOHN V C	John V C
28	710415105009	KARTHIKA B	Karthika B
29	710415105010	MAYA MOHINI B	Maya Mohini B
30	710415105011	RAHUL R	Rahul R
31	710415105012	RESHMA M	Reshma M
32	710415105301	FRANCIS JEBAK C	Francis Jebak C
33	710415105302	PRAKASH R	Prakash R
34	710415105303	RAJAPANDIYAN T	Rajapandiyam T
35	710415105304	VENKATESH M	Venkatesh M

m.j.u  
HoD / EEE



Dr.M.JEYAKUMAR, M.E..Ph.D.  
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING VAEE1801 - Simulation of Basic Element Circuits and PCB Design using EDA Tool STUDENTS ATTENDANCE LIST

S.No	REGISTER NUMBER	NAME OF THE STUDENT	18.7.18	19.7.18	20.7.18	21.7.18	23.7.18
			4-7:00 pm	4-8:00 pm	4-6:00 pm	4-6:00 pm	4-7:00 pm
1	710417105001	ABINAYA M	Abin	Abin	Abin	Abin	Abin
2	710417105002	ASWINI R	Aswini	Aswini	Aswini	Aswini	Aswini
3	710417105004	DIVYA R	Divya	Divya	Divya	Divya	Divya
4	710417105005	GEETHA V	geetha	AB	geetha	geetha	geetha
5	710417105006	GOMATHI P	Gomathi	Gomathi	Gomathi	Gomathi	Gomathi
6	710417105009	NIVETHA S	Niveetha	AB	Niveetha	AB	Niveetha
7	710417105010	SOWMIYA S	Sowmya	Sowmya	Sowmya	Sowmya	Sowmya
8	710417105011	SUBHASH B	Subhash	AB	Subhash	Subhash	Subhash
9	710416105001	ABUTHAHEER.M	Abuthaheer	Abuthaheer	Abuthaheer	Abuthaheer	Abuthaheer
10	710416105002	AKSHAY BENNY.K	Akshay	Akshay	Akshay	Akshay	Akshay
11	710416105003	ANJITHA.K.J	Anjitha	Anjitha	Anjitha	Anjitha	Anjitha
12	710416105004	ASWATHI.P.D	Aswathi	Aswathi	Aswathi	Aswathi	Aswathi
13	710416105005	BAISE.V.B	Baise	Baise	Baise	Baise	AB
14	710416105006	DENNIS ALDIRAN .A	Dennis	Dennis	Dennis	Dennis	Dennis
15	710416105007	GRACE.K	Grace	Grace	Grace	Grace	Grace
16	710416105008	KEVIN LOUIS.G	Kevin	Kevin	Kevin	Kevin	AB
17	710416105009	RENU.B	Renu	Renu	Renu	Renu	Renu
18	710416105011	VIJAY KUMAR.S	Vijay	Vijay	Vijay	AB	Vijay
19	710416105301	AJITH.S.R	Ajith	Ajith	Ajith	Ajith	Ajith
20	710415105001	AJANTHA N	Ajanta	AB	Ajanta	AB	Ajanta
21	710415105002	AKHIL MATHEW	Akhil	Akhil	Akhil	Akhil	Akhil
22	710415105003	ALAN J WALTER	ALAN	ALAN	ALAN	ALAN	ALAN
23	710415105004	ALDRIN THOMAS	ALDRIN	ALDRIN	ALDRIN	ALDRIN	ALDRIN
24	710415105005	BURUSLEE A	Burush	Burush	Burush	Burush	Burush
25	710415105006	JAYASHREE P	Jayashree	Jayashree	Jayashree	Jayashree	Jayashree
26	710415105007	JITHIN RAJ	Jithin	Jithin	Jithin	Jithin	Jithin
27	710415105008	JOHN V C	John	John	John	John	AB
28	710415105009	KARTHIKA B	Karthika	Karthika	Karthika	Karthika	Karthika
29	710415105010	MAYA MOHINI B	Maya	Maya	Maya	Maya	Maya
30	710415105011	RAHUL R	Rahul	ab	Rahul	Rahul	Rahul
31	710415105012	RESHMA M	Reshma	Reshma	Reshma	Reshma	Reshma
32	710415105301	FRANCIS JEBAK C	Francis	Francis	Francis	Francis	Francis
33	710415105302	PRAKASH R	Prakash	Prakash	Prakash	Prakash	Prakash
34	710415105303	RAJAPANDIYAN T	Rajapandiyan	Rajapandiyan	Rajapandiyan	Rajapandiyan	Rajapandiyan
35	710415105304	VENKATESH M	Venkatesh	Venkatesh	Venkatesh	Venkatesh	Venkatesh

mjk  
HoD/ EEE



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S.No	REGISTER NUMBER	NAME OF THE STUDENT	24.7.18	25.7.18	26.7.18	27.7.18	28.7.18
			4-6:00 pm	4-7:00 pm	4-7:00 pm	4-6:00 pm	4-6:00 pm
1	710417105001	ABINAYA M	Abi	Abi	Abi	Abi	Abi
2	710417105002	ASWINI R	Asw	Asw	Asw	Asw	Asw
3	710417105004	DIVYA R	Divya	Divya	Divya	Divya	Divya
4	710417105005	GEETHA V	Geetha	Geetha	Geetha	Geetha	Geetha
5	710417105006	GOMATHI P	Goma	Goma	Goma	AB	Goma
6	710417105009	NIVETHA S	Nive	Nive	Nive	Nive	Nive
7	710417105010	SOWMIYA S	Sowmi	Sowmi	Sowmi	Sowmi	Sowmi
8	710417105011	SUBHASH B	Subh	Subh	Subh	AB	Subh
9	710416105001	ABUTHAHEER.M	Abu	Abu	Abu	Abu	Abu
10	710416105002	AKSHAY BENNY.K	Aksh	Aksh	Aksh	Aksh	Aksh
11	710416105003	ANJITHA.K.J	Anji	Anji	Anji	Anji	Anji
12	710416105004	ASWATHI.P.D	Aswa	Aswa	Aswa	Aswa	Aswa
13	710416105005	BAISE.V.B	Baise	Baise	Baise	Baise	Baise
14	710416105006	DENNIS ALDIRAN .A	Deni	Deni	Deni	Deni	Deni
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17	710416105009	RENU.B	Renu	Renu	Renu	Renu	Renu
18	710416105011	VIJAY KUMAR.S	Vijay	Vijay	Vijay	Vijay	Vijay
19	710416105301	AJITH.S.R	Ajith	Ajith	Ajith	Ajith	AB
20	710415105001	AJANTHA N	Ajan	Ajan	Ajan	Ajan	Ajan
21	710415105002	AKHIL MATHEW	Akhil	Akhil	Akhil	Akhil	Akhil
22	710415105003	ALAN J WALTER	ALAN J	ALAN J	ALAN J	ALAN J	ALAN J
23	710415105004	ALDRIN THOMAS	Aldri	Aldri	Aldri	Aldri	Aldri
24	710415105005	BURUSLEE A	Burus	Burus	Burus	Burus	Burus
25	710415105006	JAYASHREE P	Jaya	Jaya	Jaya	Jaya	Jaya
26	710415105007	JITHIN RAJ	Jithin	Jithin	Jithin	Jithin	Jithin
27	710415105008	JOHN V C	John	John	John	John	John
28	710415105009	KARTHIKA B	Karth	Karth	AB	Karth	Karth
29	710415105010	MAYA MOHINI B	Maya	AB	Maya	Maya	Maya
30	710415105011	RAHUL R	Rahul	Rahul	Rahul	Rahul	Rahul
31	710415105012	RESHMA M	Resha	Resha	Resha	Resha	AB
32	710415105301	FRANCIS JEBAK C	Franc	Franc	Franc	Franc	Franc
33	710415105302	PRAKASH R	Praka	AB	Praka	Praka	Praka
34	710415105303	RAJAPANDIYAN T	Raja	Raja	Raja	Raja	Raja
35	710415105304	VENKATESH M	Venk	Venk	Venk	Venk	Venk

mju  
HoD / EEE



*(Signature)*  
Dr.M.JEYAKUMAR, M.E., Ph.D.  
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S.No	REGISTER NUMBER	NAME OF THE STUDENT	30.7.18	31.7.18	01.08.18	02.08.18	03.08.18
			4-7:00 pm	4-6:00 pm	4-6:00 pm	4-6:00 pm	4-5:00 pm
1	710417105001	ABINAYA M	Abi	ab	Abi	Abi	Abi
2	710417105002	ASWINI R	Aswin	Aswin	Aswin	Aswin	Aswin
3	710417105004	DIVYA R	Divya	Divya	Divya	Divya	Divya
4	710417105005	GEETHA V	geetha	geetha	geetha	geetha	geetha
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6	710417105009	NIVETHA S	Nivetha	Nivetha	Nivetha	Nivetha	Nivetha
7	710417105010	SOWMIYA S	Sowmya	Sowmya	Sowmya	Sowmya	Sowmya
8	710417105011	SUBHASH B	Subhash	Subhash	Subhash	Subhash	Subhash
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13	710416105005	BAISE.V.B	Baise	Baise	AB	Baise	Baise
14	710416105006	DENNIS ALDIRAN .A	Dennis	Dennis	Dennis	Dennis	Dennis
15	710416105007	GRACE.K	Grace	Grace	Grace	Grace	Grace
16	710416105008	KEVIN LOUIS.G	Kevin	Kevin	Kevin	Kevin	Kevin
17	710416105009	RENU.B	Renu	Renu	Renu	Renu	Renu
18	710416105011	VIJAY KUMAR.S	Vijay	Vijay	Vijay	Vijay	Vijay
19	710416105301	AJITH.S.R	Ajith	Ajith	Ajith	Ajith	Ajith
20	710415105001	AJANTHA N	Ajanta	Ajanta	Ajanta	Ajanta	Ajanta
21	710415105002	AKHIL MATHEW	Akhil	Akhil	Akhil	Akhil	Akhil
22	710415105003	ALAN J WALTER	ALAN J	ALAN J	ALAN J	ALAN J	ALAN J
23	710415105004	ALDRIN THOMAS	Aldrin	Aldrin	Aldrin	AB	Aldrin
24	710415105005	BURUSLEE A	AB	Burush	Burush	Burush	Burush
25	710415105006	JAYASHREE P	Jayashree	Jayashree	Jayashree	AB	Jayashree
26	710415105007	JITHIN RAJ	Jithin	Jithin	Jithin	Jithin	Jithin
27	710415105008	JOHN V C	John	John	John	John	John
28	710415105009	KARTHIKA B	Karthika	Karthika	Karthika	Karthika	Karthika
29	710415105010	MAYA MOHINI B	Maya	Maya	Maya	Maya	Maya
30	710415105011	RAHUL R	Rahul	Rahul	Rahul	Rahul	Rahul
31	710415105012	RESHMA M	Reshma	Reshma	Reshma	Reshma	Reshma
32	710415105301	FRANCIS JEBAK C	Francis	Francis	Francis	Francis	Francis
33	710415105302	PRAKASH R	Prakash	Prakash	Prakash	Prakash	Prakash
34	710415105303	RAJAPANDIYAN T	Rajapandiyan	Rajapandiyan	Rajapandiyan	Rajapandiyan	Rajapandiyan
35	710415105304	VENKATESH M	Venka	Venka	AB	Venka	Venka

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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING VAEE1801 - Simulation of Basic Element Circuits and PCB Design using EDA Tool

### MULTIPLE CHOICE QUESTIONS

1. Which of the following is a feature of EasyEDA tool?
  - a) Circuit simulation
  - b) PCB layout design
  - c) Schematic capture
  - d) All of the above
  - e) None of the above
2. EasyEDA allows users to simulate circuits based on which type of analysis?
  - a) AC analysis
  - b) DC analysis
  - c) Transient analysis
  - d) All of the above
  - e) Only a) and b)
3. What is the primary purpose of PCB design in EasyEDA?
  - a) To visualize the circuit
  - b) To design the enclosure
  - c) To create a layout for manufacturing
  - d) To simulate the circuit
  - e) none of the above.
4. Which file format is commonly used for exporting PCB designs from EasyEDA?
  - a) .pdf
  - b) .png
  - c) .gerber
  - d) .docx
  - e) .txt
5. How can components be added to a schematic in EasyEDA?
  - a) By dragging and dropping from a component library
  - b) By typing code
  - c) By using voice commands
  - d) By importing images
  - e) By drawing with a pen tool

  
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6. What feature of EasyEDA allows users to collaborate on projects?
  - a) Version control
  - b) Built-in chat
  - c) File sharing
  - d) Real-time editing
  - e) **All of the above**
  
7. Which of the following is NOT a step in PCB design using EasyEDA?
  - a) Schematic capture
  - b) Component placement
  - c) **Gerber files import**
  - d) Routing traces
  - e) Design rule check
  
8. In EasyEDA, what does the Design Rule Check (DRC) feature do?
  - a) Simulates the circuit
  - b) **Checks for errors in the PCB design**
  - c) Converts schematic to PCB layout
  - d) Generates gerber files
  - e) None of the above
  
9. Which of the following is a benefit of using EasyEDA for electronic circuit design?
  - a) **Free access**
  - b) No need for internet connection
  - c) Limited component library
  - d) No simulation capabilities
  - e) Only a) and c)
  
10. What is the purpose of generating Gerber files in EasyEDA?
  - a) **To export the PCB design for manufacturing**
  - b) To create a 3D model of the circuit
  - c) To print the schematic
  - d) To simulate the circuit
  - e) None of the above



  
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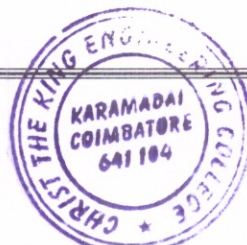
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING VAEE1801 - Simulation of Basic Element Circuits and PCB Design using EDA Tool STUDENTS ASSESSMENT MARKS

S.No	REGISTER NUMBER	NAME OF THE STUDENT	MARKS
			10
1	710417105001	ABINAYA M	9
2	710417105002	ASWINI R	9
3	710417105004	DIVYA R	8
4	710417105005	GEETHA V	9
5	710417105006	GOMATHI P	7
6	710417105009	NIVETHA S	8
7	710417105010	SOWMIYA S	8
8	710417105011	SUBHASH B	7
9	710416105001	ABUTHAHEER.M	7
10	710416105002	AKSHAY BENNY.K	8
11	710416105003	ANJITHA.K.J	9
12	710416105004	ASWATHI.P.D	8
13	710416105005	BAISE.V.B	7
14	710416105006	DENNIS ALDIRAN .A	9
15	710416105007	GRACE.K	8
16	710416105008	KEVIN LOUIS.G	7
17	710416105009	RENU.B	7
18	710416105011	VIJAY KUMAR.S	6
19	710416105301	AJITH.S.R	7
20	710415105001	AJANTHA N	8
21	710415105002	AKHIL MATHEW	10
22	710415105003	ALAN J WALTER	10
23	710415105004	ALDRIN THOMAS	8
24	710415105005	BURUSLEE A	7
25	710415105006	JAYASHREE P	9
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35	710415105304	VENKATESH M	8

m.j.u  
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*M. Jeyakumar*  
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

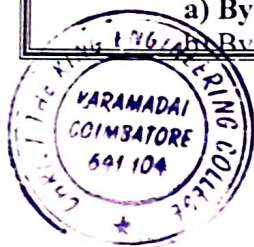
### VAEE1801 - SIMULATION OF BASIC ELECTRONIC CIRCUITS AND PCB DESIGN USING EASY EDA TOOL

#### MULTIPLE CHOICE QUESTIONS

Name : JOHN.VC  
Register No. : 710415105008  
Year / Sem : II/III  
Academic Year : 2018-19

1. Which of the following is a feature of EasyEDA tool?  
a) Circuit simulation  
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c) Schematic capture  
d) **All of the above**  
e) None of the above
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- c) By using voice commands  
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e) By drawing with a pen tool
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10/10

  
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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

## STUDENT FEEDBACK FORM

Name of the value added course: *Simulation of basic electronic Circuits and PCB design using easy EDA tool.*  
Name of the student: *Rahul R*  
Register Number: *710415105011*  
Year / Semester: *II / III*  
Batch: *2017-2021*  
Course Coordinator: *Mr. D. Atharathan*  
Academic Year: *2018-19*

Sl.No.	Criteria	Rating				
		Excellent	Good	Average	Satisfactory	Poor
1.	Course content		✓			
2.	Individual attention			✓		
3.	Level of the course		✓			
4.	Regularity and knowledge of faculty	✓				
5.	Coverage of syllabus		✓			
6.	Interaction	✓				
7.	Outcome		✓			
	Total		✓			
	Remarks		✓			

EXCELLENT-5

GOOD-4

AVERAGE-3

SATISFACTORY-2

POOR-1



*Rahul*

Signature of the student

*[Signature]*  
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## POST EVENT REPORT

Course code	VAEE1801
Name of the course	SIMULATION OF BASIC ELECTRONIC CIRCUITS AND PCB DESIGN USING EASY EDA TOOL
Duration of the course	35 Hours
Academic year	2018-2019
Period of the course	15 Days
Coordinator & Faculty handling the course	Mr.D. Citharthan AP/EEE & Mr.G. Gokul kumar, AP/EEE
Number of the students registered	35
Number of the students completed	35
% of students completed the course	100%
Details of the course	To know the fundamentals of electronic circuits, design the circuit and create printed circuit board using EasyEDA software.



  
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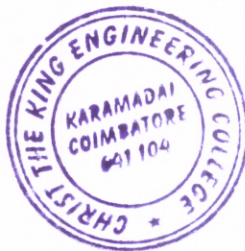
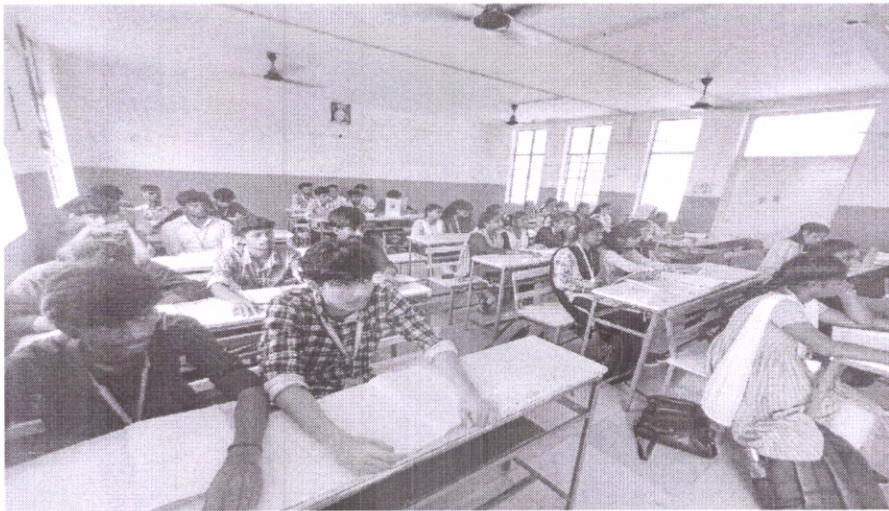
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PHOTO



  
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## CERTIFICATES



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